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PTO/SB/21 (12/97)  
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Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

		Application No.	09/262,458
		Filing Date	March 4, 1999
		First Named Inventor	Brian D. Possley
		Group Art Unit	2814
		Examiner Name	N. Ngo
Total Number of Pages in This Submission	10	Attorney Docket Number	42390P6643

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### ENCLOSURES (check all that apply)

<input checked="" type="checkbox"/> Fee Transmittal Form  <input checked="" type="checkbox"/> Fee Attached  <input checked="" type="checkbox"/> Amendment / Response  <input checked="" type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s)  <input type="checkbox"/> Extension of Time Request  <input type="checkbox"/> Express Abandonment Request  <input type="checkbox"/> Information Disclosure Statement  <input type="checkbox"/> Certified Copy of Priority Document(s)  <input type="checkbox"/> Response to Missing Parts/ Incomplete Application  <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application)  <input type="checkbox"/> Drawing(s)  <input type="checkbox"/> Licensing-related Papers  <input type="checkbox"/> Petition Routing Slip (PTO/SB/69) and Accompanying Petition  <input type="checkbox"/> To Convert a Provisional Application  <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address  <input type="checkbox"/> Terminal Disclaimer  <input type="checkbox"/> Small Entity Statement  <input type="checkbox"/> Request for Refund	<input type="checkbox"/> After Allowance Communication to Group  <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences  <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief)  <input type="checkbox"/> Proprietary Information  <input type="checkbox"/> Status Letter  <input type="checkbox"/> Additional Enclosure(s) (please identify below):
Remarks		

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Gregory D. Caldwell, Reg. No. 39,926  BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	May 7, 2001

### CERTIFICATE OF MAILING/TRANSMISSION

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Typed or printed name	Angie C. Farr		
Signature		Date	May 7, 2001

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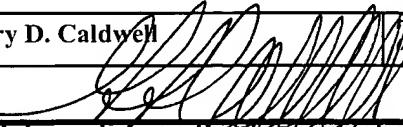
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METHOD OF PAYMENT (check one)					FEE CALCULATION (continued)																																																																																																																																																																																																										
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ADDITIONAL FEE</p> <table border="1"> <thead> <tr> <th colspan="2">Large Entity</th> <th colspan="3">Small Entity</th> <th rowspan="2">Fee Description</th> <th rowspan="2">Fee Paid</th> </tr> <tr> <th>Fee Code</th> <th>Fee (\$)</th> <th>Fee Code</th> <th>Fee (\$)</th> <th></th> </tr> </thead> <tbody> <tr> <td>105</td> <td>130</td> <td>205</td> <td>65</td> <td>Surcharge - late filing fee or oath</td> <td></td> </tr> <tr> <td>127</td> <td>50</td> <td>227</td> <td>25</td> <td>Surcharge - late provisional filing fee or cover sheet.</td> <td></td> </tr> <tr> <td>139</td> <td>130</td> <td>139</td> <td>130</td> <td>Non-English specification</td> <td></td> </tr> <tr> <td>147</td> <td>2,520</td> <td>147</td> <td>2,520</td> <td>For filing a request for reexamination</td> <td></td> </tr> <tr> <td>112</td> <td>920*</td> <td>112</td> <td>920*</td> <td>Requesting publication of SIR prior to Examiner action</td> <td></td> </tr> <tr> <td>113</td> <td>1,840*</td> <td>113</td> <td>1,840*</td> <td>Requesting publication of SIR after Examiner action</td> <td></td> </tr> <tr> <td>115</td> <td>110</td> <td>215</td> <td>55</td> <td>Extension for response within first month</td> <td></td> </tr> <tr> <td>116</td> <td>390</td> <td>216</td> <td>195</td> <td>Extension for response within second month</td> <td></td> </tr> <tr> <td>117</td> <td>890</td> <td>217</td> <td>445</td> <td>Extension for response within third month</td> <td></td> </tr> <tr> <td>118</td> <td>1,390</td> <td>218</td> <td>695</td> <td>Extension for response within fourth month</td> <td></td> </tr> <tr> <td>128</td> <td>1,890</td> <td>228</td> <td>945</td> <td>Extension for response within fifth month</td> <td></td> </tr> <tr> <td>119</td> <td>310</td> <td>219</td> <td>155</td> <td>Notice of Appeal</td> <td></td> </tr> <tr> <td>120</td> <td>310</td> <td>220</td> <td>155</td> <td>Filing a brief in support of an appeal</td> <td></td> </tr> <tr> <td>121</td> <td>270</td> <td>221</td> <td>135</td> <td>Request for oral hearing</td> <td></td> </tr> <tr> <td>138</td> <td>1,510</td> <td>138</td> <td>1,510</td> <td>Petition to institute a public use proceeding</td> <td></td> </tr> <tr> <td>140</td> <td>110</td> <td>240</td> <td>55</td> <td>Petition to revive - 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SUBMITTED BY		Complete (if applicable)		
Name (Print/Type)	Gregory D. Caldwell	Registration No. (Attorney/Agent)	39,926	Telephone (503) 684-6200
Signature			Date	05/07/01

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In re CPA of:

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Possley ✓

Application No.: 09/262,458 ✓

Filed: March 4, 1999 ✓

For: GATE ARRAY ARCHITECTURE

Examiner: N. Ngo

Art Unit: 2814

**EXPEDITED PROCEDURE****UNDER 37 CFR 1.116****ART UNIT: 2814**RECEIVED  
MAY 15 2001  
TC 2800 MAIL ROOMM. Branson  
10/15/02**AMENDMENT UNDER 37 CFR 1.116**

Box AF  
 ASSISTANT COMMISSIONER FOR PATENTS  
 Washington, D.C. 20231

Dear Sir:

In response to the Final Office Action, dated March 7, 2001, please enter the following amendment and consider the following remarks. A "Marked Version of the Claims" to show the changes is attached hereto.

**IN THE CLAIMS:**

Please amend the claims to read as follows:

1. An integrated circuit comprising: a gate array architecture;

*said gate array architecture including a semiconductor substrate having a plurality of N-type diffusion regions and P-type diffusion regions, said diffusion regions having partially overlying polysilicon landing sites, at least one forming both N-type and P-type transistors;*  
*wherein the regions are relatively-sized to form two distinct transistor sizes, smaller N- and P-type transistors and larger N- and P-type transistors.*